| | Subclass | |
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| | Class | ISSUE CLASSIFICATION |

879

PATENT NUMBER

## U.S. **UTILITY** Patent Application

O.I.P.E.

SCANNED Q.A.

PATENT DATE

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
|---|---|---|---|---|---|
| 09/742171 | | 716 | 7 | 2825 | THOMPSON |

**APPLICANTS**

Steven Teig
Joseph Ganley

**TITLE**

Method and apparatus for quantifying the quality of placement
configurations in a partitioned region of an integrated-circuit
layout

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## ISSUING CLASSIFICATION

| ORIGINAL | | | CROSS REFERENCE(S) | | | | | |
|---|---|---|---|---|---|---|---|---|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | |
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| ☐ **TERMINAL DISCLAIMER** | DRAWINGS | | | CLAIMS ALLOWED | |
|---|---|---|---|---|---|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| ☐ The term of this patent subsequent to _____ (date) has been disclaimed. | (Assistant Examiner)         (Date) | | | **NOTICE OF ALLOWANCE MAILED** | |
| ☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | (Primary Examiner)          (Date) | | | **ISSUE FEE** | |
| | | | | Amount Due | Date Paid |
| ☐ The terminal ____ months of this patent have been disclaimed. | (Legal Instruments Examiner)     (Date) | | | **ISSUE BATCH NUMBER** | |

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